


EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	128	703/13.ccls. and @pd>="20071201"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/29 17:28
L5	1099	(virtual adj machine) and monitor and translat\$5 and (operating adj system) and @ad<"20020101"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/29 17:42


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Analysis of the Intel Pentium's ability to support a secure **virtual machine monitor** - all 10 versions »

JS Robin, CE Irvine - Proceedings of the 9th conference on USENIX Security ..., 2000 - portal.acm.org
 ... Examples include a protection primitive, address **translation** primitive, or a sub ...
 VMM requirements, it can still implement a hybrid **virtual machine monitor** (HVM) ...
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Virtualization system including a **virtual machine monitor** for a computer with a segmented ... - all 3 versions »

S Devine, E Bugnion, M Rosenblum - US Patent 6,397,242, 1998 - freepatentsonline.com
 ... directly executable VM instructions and non-directly executable instructions; in which: the **virtual machine monitor** includes: a binary **translation** sub-system ...
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VM/4: ACOS-4 **virtual machine** architecture

S Nanba, N Ohno, H Kubo, H Morisue, T Ohshima, H ... - ACM SIGARCH Computer Architecture News, 1985 - portal.acm.org
 ... from the **virtual machine** and execute them with address **translation** from the ... it is possible to reduce the number of **virtual machine monitor** interventions and ...
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The Jalapeno **virtual machine** - all 13 versions »

B Alpern, CR Altanasio, JJ Barton, MG Burke, P ... - IBM Systems Journal, 2000 - research.ibm.com
 ... The optimizing compiler's front end (**translation** to HIR ... the overall design of such a **virtual machine**. ... line measurement subsystem will **monitor** the performance ...
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Translation buffer for virtual machines with address space match - all 7 versions »

AH Mason, JS Hall, PT Robinson, RT Witek - US Patent 5,319,760, 1994 - Google Patents
 ... context switching from one **virtual machine** to another, or to or from the **virtual machine monitor**, needlessly flushing entries in the **translation buffer** which ...
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Protection ring extension for computers having distinct **virtual machine monitor and **virtual machine** ... - all 3 versions »**

PT Robinson, AH Mason, JS Hall - US Patent 5,522,075, 1996 - freepatentsonline.com
 ... 7, 1994, and entitled "**TRANSLATION BUFFER FOR VIRTUAL MACHINES WITH ADDRESS ...** having multiple protection rings that utilize a **virtual machine monitor** to provide ...
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Channel address control system for a **virtual machine system - all 3 versions »**

S Kaneda, M Ishibashi, Y Seta, F Ikegami - US Patent 4,459,661, 1984 - freepatentsonline.com
 ... **translation** table data and instruction signal data, said plural control programs being operated on the **virtual machine system** under the control of the **monitor** ...
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Application and analysis of the **virtual machine** approach to information system security and



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[Analysis of the Intel Pentium's ability to support a secure **virtual machine monitor**](#) - all 10 versions »

JS Robin, CE Irvine - Proceedings of the 9th conference on USENIX Security ..., 2000 - portal.acm.org
... can still implement a hybrid **virtual machine monitor** (HVM ... tecton system or an address translation system to ... that specifies the privilege level of the segment ...

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[Virtualization system including a **virtual machine monitor** for a computer with a segmented ...](#) - all 3 versions »

S Devine, E Bugnion, M Rosenblum - US Patent 6,397,242, 1998 - freepatentsonline.com
... structure and function of a **segment register** ... incorporates a system-level **virtual machine monitor** (VMM) according ... engines for both binary translation and direct ...

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[VM/4: ACOS-4 **virtual machine architecture**](#)

S Nanba, N Ohno, H Kubo, H Morisue, T Ohshima, H ... - ACM SIGARCH Computer Architecture News, 1985 - portal.acm.org

... which is the VM/k **virtual machine monitor**, is notified ... the physical address within the **virtual machine** is used ... machine, using the address translation table for ...

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[Channel address control system for a **virtual machine system**](#) - all 3 versions »

S Kaneda, M Ishibashi, Y Seta, F Ikegami - US Patent 4,459,661, 1984 - freepatentsonline.com
... programs being operated on the **virtual machine system** under the control of the **monitor** program, said ... a dynamic address translation adder circuit, having ...

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[Capability-Based Addressing](#) - all 6 versions »

RS Fabry - Communications, 1974 - portal.acm.org

... A hardware address translation scheme which never requires the modification of the ... scheme uses a main memory hash table for obtaining a **segment's** location in ...

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[Virtual machine system with address translation buffer for holding host and plural guest entries](#) - all 3 versions »

US Patent 4,816,991, 1989 - freepatentsonline.com

... ID is managed by the VM **monitor** of software ... parts of a CPU of a **virtual machine system** which ... that the associated entry is address translation information which ...

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[Translation buffer for virtual machines with address space match](#) - all 7 versions »

AH Mason, JS Hall, PT Robinson, RT Witek - US Patent 5,319,760, 1994 - Google Patents

... context switching from one **virtual machine** to another, or to or from the **virtual machine monitor**, needlessly flushing entries in the translation buffer which ...

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